

# 6. Status Register (SREG)

The Status Register contains flags that indicate the results of arithmetic/logic operations. This register is crucial for branch instructions.

Bit	Name	Description
7	<b>I</b> (Global Interrupt Enable)	Enables/disables global interrupts
6	<b>T</b> (Bit Copy Storage)	Storage for BLD/BST instructions
5	<b>H</b> (Half Carry Flag)	Carry from bit 3 to bit 4 (for BCD)
4	<b>S</b> (Sign Flag)	$S = N \oplus V$ (for signed operations)
3	<b>V</b> (Overflow Flag)	Two's complement overflow
2	<b>N</b> (Negative Flag)	Result is negative (bit 7 = 1)
1	<b>Z</b> (Zero Flag)	Result = 0
0	<b>C</b> (Carry Flag)	Carry/borrow from operation

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