

# Persiapan Praktikum Semester Baru

- [Surat Menyurat](#)
- [Template EMAS](#)

# Surat Menyurat

# Template EMAS

## Header EMAS 1 - Literal Header

```
<p dir="ltr" style="text-align: center;"></p>
<p dir="ltr" style="text-align: left;"></p>
<h2 style="text-align: center;">Digital Laboratory DTE FTUI</h2>
<h2 style="text-align: center;"><b style="font-size: 2.46rem;">PRAKTIKUM DASAR SISTEM
DIGITAL</b></h2>
<h2 style="text-align: center;"><b style="font-size: 2.46rem;">TEKNIK KOMPUTER
REGULER&nbsp;</b></h2>
<h2 style="text-align: center;"><b style="font-size: 2.46rem;">SEMESTER GANJIL
2025/2026</b></h2>
<p style="text-align: center;"><br></p>
<p></p>
```

## Header EMAS 2 - Informasi Header

```
<table width="100%" border="0" cellpadding="20">
  <tbody>
    <tr>
      <td valign="top" width="50%">
        <h3>Kepala Laboratorium :</h3>
        <ul>
          <li>Dr. Eng. Mia Rizkinia, S.T., M.T.</li>
        </ul>
        <h3>Koordinator Laboratorium :</h3>
        <ul>
          <li>Giovan Christoffel Sihombing (GI)</li>
        </ul>
        <h3>Koordinator Praktikum :</h3>
        <ul>
          <li>Novan Agung Wicaksono (NA)</li>
        </ul>
      </td>
    </tr>
  </tbody>
</table>
```

<h3>Laboratory Assistant :</h3>

<ol>

<li>Giovan Christoffel Sihombing (GI)</li>

<li>Evandita Wiratama Putra (EZ)</li>

<li>Edgrant Henderson Suryajaya (ED)</li>

<li>Phoebe Ivana (PI)</li>

<li>Wendy Dharmawan (WN)</li>

<li>Nicholas Samosir (NS)</li>

<li>Ivan Yuantama Pradipta (YP)</li>

<li>Bryan Herdianto (BH)</li>

<li>Christian Hadiwijaya (CH)</li>

<li>Daffa Sayra Firdaus (DS)</li>

<li>Jesaya David Gamalael Nickhelsen Panggabean (JD)</li>

<li>Muhammad Nadzhif Fikri (MF)</li>

<li>Alexander Christian (AX)</li>

<li>Muhammad Riyan Satrio Wibowo (RE)</li>

<li>Novan Agung Wicaksono (NA)</li>

<li>Nabil Putra Nurfariz (NZ)</li>

<li>Muhammad Dhiya'ulhaq (DY)</li>

<li>Carlsson Khovis (KH)</li>

<li>Khalisa Zahra Maulana (KZ)</li>

</ol>

</td>

<td valign="top" width="50%">

<h3>Modul Praktikum</h3>

<ol>

<li>Introduction to Digital Circuit</li>

<li>Boolean Algebra & Basic Logic Gates</li>

<li>Karnaugh Map</li>

<li>Complex Logic Gates</li>

<li>Decoder & Encoder</li>

<li>Multiplexer & Demultiplexer</li>

<li>Arithmetic Circuit</li>

<li>Flip-Flop & Latch</li>

<li>Register & Counter</li>

<li>Proyek Akhir</li>

</ol>

### <h3>Lab Activities Assessment Weight</h3>

<ul>

<li>Tugas Pendahuluan (TP) : 30%</li>

<li>Case Study (CS) : 50%</li>

<li>Tugas Tambahan (TT) : 20%</li>

</ul>

### <h3>Contacts</h3>

<ul>

<li>OA Line : @digilabui</li>

<li>Instagram : <a href="https://www.instagram.com/digilabui/"

target="\_blank">@digilabui</a></li>

<li>YouTube : <a

href="https://www.youtube.com/@digitallaboratorydteftui811" target="\_blank">Digital Laboratory

UI</a></li>

</ul>

</td>

</tr>

</tbody>

</table>